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PLUS Search Results for S/N 10056631, Searched September 20, 2004

The Patent Linguistics Utility System (PLUS) is a USPTO automated search system for U.S. Patents from 1971 to the present. PLUS is a query-by-example search system which produces a list of patents that are most closely related linguistically to the application searched. This search was prepared by the staff of the Scientific and Technical Information Center, SIRA.

5623645
6115551
5666321
5751656
5912860
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5954838
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5623645 80
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5818777 70
6442643 70
5469547 69
6088812 69
6163819 69
6145019 68
5848264 67
5910917 67
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5367149 64
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5859986 64
6266731 63
5897654 63
5778196 63
4803475 63

5034917 63
5852579 63

10056631_CLS
Most Frequently Occurring Classifications of Patents Returned From A Search of 10056631 on September 20, 2004

Original Classifications

4	365/233.5
2	365/193
2	365/233
2	710/104
2	710/107
2	710/62
2	711/167
2	713/400
2	713/401
2	713/600

Cross-Reference Classifications

4	365/203
4	365/233
3	365/236
3	710/9
2	326/27
2	365/194
2	365/221
2	365/230.03
2	365/230.06
2	365/230.08
2	710/100
2	710/105
2	710/107
2	710/240
2	710/36
2	711/133
2	711/168

Combined Classifications

6	365/233
4	365/203
4	365/233.5
4	710/107
3	365/236
3	710/9
3	713/600
2	326/27
2	326/30
2	365/189.05
2	365/193
2	365/194

2 365/221
2 365/230.03
2 365/230.06
2 365/230.08
2 710/100
2 710/104
2 710/105
2 710/240
2 710/29
2 710/313
2 710/36
2 710/62
2 711/133
2 711/167
2 711/168
2 713/400
2 713/401

10056631_CLSTITLES

Titles of Most Frequently Occurring Classifications of Patents Returned

From A Search of 10056631 on September 20, 2004

6 365/233 (2 OR, 4 XR)
Class 365 : STATIC INFORMATION STORAGE AND RETRIEVAL
365/230.01 ADDRESSING
365/233 .Sync/clocking

4 365/203 (0 OR, 4 XR)
Class 365 : STATIC INFORMATION STORAGE AND RETRIEVAL
365/189.01 READ/WRITE CIRCUIT
365/203 .Precharge

4 365/233.5 (4 OR, 0 XR)
Class 365 : STATIC INFORMATION STORAGE AND RETRIEVAL
365/230.01 ADDRESSING
365/233 .Sync/clocking
365/233.5 ..Transition detection

4 710/107 (2 OR, 2 XR)
Class 710 : ELECTRICAL COMPUTERS AND DIGITAL DATA PROCESSING SYSTEMS: INPUT/OUTPUT
710/100 INTRASYSTEM CONNECTION (E.G., BUS AND BUS TRANSACTION PROCESSING)
710/107 .Bus access regulation

3 365/236 (0 OR, 3 XR)
Class 365 : STATIC INFORMATION STORAGE AND RETRIEVAL
365/230.01 ADDRESSING
365/236 .Counting

3 710/9 (0 OR, 3 XR)
Class 710 : ELECTRICAL COMPUTERS AND DIGITAL DATA PROCESSING SYSTEMS: INPUT/OUTPUT
710/1 INPUT/OUTPUT DATA PROCESSING
710/8 .Peripheral configuration
710/9 ..Address assignment

3 713/600 (2 OR, 1 XR)
Class 713 : ELECTRICAL COMPUTERS AND DIGITAL PROCESSING SYSTEMS: SUPPORT
713/600 CLOCK CONTROL OF DATA PROCESSING SYSTEM, COMPONENT, OR DATA TRANSMISSION

2 326/27 (0 OR, 2 XR)
Class 326 : ELECTRONIC DIGITAL LOGIC CIRCUITRY

326/21 SIGNAL SENSITIVITY OR TRANSMISSION INTEGRITY
326/26 .Output switching noise reduction
326/27 ..With field effect-transistor

2 326/30 (1 OR, 1 XR)
Class 326 : ELECTRONIC DIGITAL LOGIC CIRCUITRY
326/21 SIGNAL SENSITIVITY OR TRANSMISSION INTEGRITY
326/30 .Bus or line termination (e.g., clamping, impedance matching, etc.)

2 365/189.05 (1 OR, 1 XR)
Class 365 : STATIC INFORMATION STORAGE AND RETRIEVAL
365/189.01 READ/WRITE CIRCUIT
365/189.05 .Having particular data buffer or latch

2 365/193 (2 OR, 0 XR)
Class 365 : STATIC INFORMATION STORAGE AND RETRIEVAL
365/189.01 READ/WRITE CIRCUIT
365/191 .Signals
365/193 ..Strobe

2 365/194 (0 OR, 2 XR)
Class 365 : STATIC INFORMATION STORAGE AND RETRIEVAL
365/189.01 READ/WRITE CIRCUIT
365/191 .Signals
365/194 ..Delay

2 365/221 (0 OR, 2 XR)
Class 365 : STATIC INFORMATION STORAGE AND RETRIEVAL
365/189.01 READ/WRITE CIRCUIT
365/221 .Serial read/write

2 365/230.03 (0 OR, 2 XR)
Class 365 : STATIC INFORMATION STORAGE AND RETRIEVAL
365/230.01 ADDRESSING
365/230.03 .Plural blocks or banks

2 365/230.06 (0 OR, 2 XR)
Class 365 : STATIC INFORMATION STORAGE AND RETRIEVAL
365/230.01 ADDRESSING
365/230.06 .Particular decoder or driver circuit

2 365/230.08 (0 OR, 2 XR)
Class 365 : STATIC INFORMATION STORAGE AND RETRIEVAL
365/230.01 ADDRESSING
365/230.08 .Including particular address buffer or latch circuit arrangement

2 710/100 (0 OR, 2 XR)
Class 710 : ELECTRICAL COMPUTERS AND DIGITAL DATA PROCESSING SYSTEMS: INPUT/OUTPUT
710/100 INTRASYSTEM CONNECTION (E.G., BUS AND BUS TRANSACTION PROCESSING)

2 710/104 (2 OR, 0 XR)
Class 710 : ELECTRICAL COMPUTERS AND DIGITAL DATA PROCESSING SYSTEMS: INPUT/OUTPUT
710/100 INTRASYSTEM CONNECTION (E.G., BUS AND BUS TRANSACTION PROCESSING)
710/104 .System configuring

2 710/105 (0 OR, 2 XR)
Class 710 : ELECTRICAL COMPUTERS AND DIGITAL DATA PROCESSING SYSTEMS: INPUT/OUTPUT
710/100 INTRASYSTEM CONNECTION (E.G., BUS AND BUS TRANSACTION PROCESSING)
710/105 .Protocol

2 710/240 (0 OR, 2 XR)
Class 710 : ELECTRICAL COMPUTERS AND DIGITAL DATA PROCESSING SYSTEMS: INPUT/OUTPUT
710/240 ACCESS ARBITRATING

2 710/29 (1 OR, 1 XR)
Class 710 : ELECTRICAL COMPUTERS AND DIGITAL DATA PROCESSING SYSTEMS: INPUT/OUTPUT
710/1 INPUT/OUTPUT DATA PROCESSING
710/29 .Flow controlling

2 710/313 (1 OR, 1 XR)
Class 710 : ELECTRICAL COMPUTERS AND DIGITAL DATA PROCESSING SYSTEMS: INPUT/OUTPUT
710/100 INTRASYSTEM CONNECTION (E.G., BUS AND BUS TRANSACTION PROCESSING)
710/305 .Bus interface architecture
710/306 ..Bus bridge
710/313 ...Peripheral bus coupling (e.g., PCI, USB, ISA, and etc.)

2 710/36 (0 OR, 2 XR)
Class 710 : ELECTRICAL COMPUTERS AND DIGITAL DATA PROCESSING SYSTEMS: INPUT/OUTPUT
710/1 INPUT/OUTPUT DATA PROCESSING
710/36 .Input/Output access regulation

2 710/62 (2 OR, 0 XR)

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Class 710 : ELECTRICAL COMPUTERS AND DIGITAL DATA PROCESSING SYSTEMS: INPUT/OUTPUT
710/1 INPUT/OUTPUT DATA PROCESSING
710/62 .Peripheral adapting

2 711/133 (0 OR, 2 XR)
Class 711 : ELECTRICAL COMPUTERS AND DIGITAL PROCESSING SYSTEMS: MEMORY
711/100 STORAGE ACCESSING AND CONTROL
711/117 .Hierarchical memories
711/118 ..Caching
711/133 ...Entry replacement strategy

2 711/167 (2 OR, 0 XR)
Class 711 : ELECTRICAL COMPUTERS AND DIGITAL PROCESSING SYSTEMS: MEMORY
711/100 STORAGE ACCESSING AND CONTROL
711/167 .Access timing

2 711/168 (0 OR, 2 XR)
Class 711 : ELECTRICAL COMPUTERS AND DIGITAL PROCESSING SYSTEMS: MEMORY
711/100 STORAGE ACCESSING AND CONTROL
711/167 .Access timing
711/168 ..Concurrent accessing

2 713/400 (2 OR, 0 XR)
Class 713 : ELECTRICAL COMPUTERS AND DIGITAL PROCESSING SYSTEMS: SUPPORT
713/400 SYNCHRONIZATION OF CLOCK OR TIMING SIGNALS, DATA, OR PULSES

2 713/401 (2 OR, 0 XR)
Class 713 : ELECTRICAL COMPUTERS AND DIGITAL PROCESSING SYSTEMS: SUPPORT
713/400 SYNCHRONIZATION OF CLOCK OR TIMING SIGNALS, DATA, OR PULSES
713/401 .Using delay